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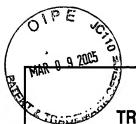
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TRANSMITTAL FORM

Total Number of Pages in this Submission

Application Number	10/074,732	7 3
Filing Date	February 13, 2002	
First Named Inventor	Leo Mathew et al.	
Group Art Unit	2815	
Examiner Name	Paul E. Brock II	
Attorney Docket Number	SC11805TP	

	*	ENCLOSURES	(check a	II that apply)
Fee Transmittal F	Form	Assignment Papers		llowance
Fee Attac	hed	(for an Application) Drawing(s)	Appea	nunication to Group I Communication to Board eals and Interferences
Amendment/Rep	ly	Licensing-Related papers	Appea	I Communication to Group I Notice, Brief, Reply Brief)
After Fina	ı	Petition		etary Information
Affidavits/	Declaration(s)	Petition to Convert to a Provisional Application	Status	Letter with appropriate copies
Extension of time	Request	Power of Attorney, Revocation, Change of Correspondence	<u> </u>	nclosure(s) (please identify below)
Express Abandor	nment Request	Address		sociate Power of Attorney
	osure Statement, eferences DA-DM	Terminal Disclaimer	· —	tum Postal Card
Certified Copy of	Priority Documents	Request for Refund	-	
Response to Miss	sing Parts/	CD, Number of CDs		
Incomplete Applic	cation	Remarks		
	e to Missing Parts CFR 1.52 or 1.53			
	SIGNATURI	E OF APPLICANT, ATTORNEY, (OR AGENT	
Firm or Robert	L. King		Registration No.	30,185
Signature	Robert	L. King		
Date /	March 3,	2005		
		CERTIFICATE OF MAILING		
		deposited with the United States Postal s mmissioner for Patents, PO Box 1450, Al		
Typed or printed name	Pat Thomas			
Signature	Yas 4ll	loma s	Date	3-3-05



TOTAL AMOUNT OF PAYMENT

FEE TRANSMITTAL

Patent fees are subject to annual revision

(\$) 180

	Complete if Known
Application Number	10/074,732
Filing Date	February 13, 2002
First Named Inventor	Leo Mathew et al.
Examiner Name	Paul E. Brock II
Group Art Unit	2815
Attorney Docket No.	SC11805TP

METHOD O	FPAYMENT			FE	E CALC	CULATION (continue	ed)	
The Commissioner is hereby authorize	ed to charge indicated fees and		OITIONA					
credit any overpayment to: Deposit Account Number	503079	Lar Eni		Sm <u>En</u>				
Deposit Account Name	Freescale Semiconductor, Inc.	Fee	Fee	Fee	Fee			
1	nic.	Code	(\$)	Code	(\$)	Fee Description		
X Charge Any Additional Fee required u	inder 37 CFR 1.16 and 1.17	1051	130	2051	65	Surcharge - late filing fee of	or oath	
Applicant claims small entity status. S	See 37 CFR 1.27	1052 1053	50 130	2052 1053	25 130	Surcharge – late Provisiona Non-English specification	al filing	
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2. Payment Enclosed:		1804	920°	1804	920*	Requesting publication of SIR p Examiner action	rior to	
Check Credit Ca	rd Money Order Other	1805	1840*	1805	1840*	Requesting publication of S Examiner action	IR after	
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Large Entity Small Entity Fee Fee Fee Fee		1401	320	2402	160	Filing a brief in support of a	n appeal	
Code (\$) Code (\$)	Fee Paid	1403	280	2403	140	Request for oral hearing Petition to institute a public		
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1001 750 2001 375	Utility filing fee	1452 1453	110 1300	2452 2453	55 650	Petition to revive - unavoida Petition to revive - unintenti		
1002 330 2002 165 1003 520 2003 260	Design filing fee Plant filing fee	1501	1300	2501	650	Utility issue fee (or reissue)	-	
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1005 160 2005 80	Provisional filing fee	1503	630	2503	315	Plant issue fee	l l	
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	SUBTOTAL (1) (\$)	1807	50	1807	50	Processing fee under 37 CF	R 1.17(q)	
2. EXTRA CLAIM FEES		1806	180	1806	180	Submission of IDS		180
Previously Paid**	Extra Fee from Claims below Fee Paid	8021	40	8021	40	Recording each patent assi per property (times number of p	properties)	
Total Claims - 20 Independent Claims - 3	= X 18 = X 84 =	1809	750	2809	375	Filing a submission after fin rejection (37 CFR § 1.129(a	a)) _	
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SUBMITTED BY				-		Complete (i	f applicable)	
Name (Print/Type) Robert L.	King a a	Registra	ation No.	30,18	85	Telephone	(512) 996-6	839
Signature (Fillio Type) Frobert C.	Rett		13/0			releptions	1	
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:
Application No.:

Leo Mathew et al.

Group Art Unit:

2815

Application N Date Filed: 10/074,732 February 13, 2002 Examiner: Docket No.: Paul E. Brock II SC11805TP

Title: METHOD OF FORMING A VER AND STRUCTURE THEREOF

METHOD OF FORMING A VERTICAL DOUBLE GATE SEMICONDUCTOR DEVICE

CERTIFICATE OF MAILING

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO:	
Date of Deposit	_
FREESCALE SEMICONDUCTOR, INC.	_
Name of Assignee	
Pat Thomas Yay Ywmas	_
Printed Name of Person Signing Certificate	

INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

In accordance with 37 C.F.R. §1.56 and in compliance with 37 C.F.R. §§1.97 and 1.98, the references listed on attached Form PTO/SB/08 and/or subsequently identified herein, are being submitted herewith for consideration by the United States Patent and Trademark Office. The Office hereby waives the requirement under 37 CFR 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003. See 37 CFR 1.491(b).

COP	IES.

a. A legible copy of (i) each U.S. and foreign patent; (ii) each publication or that portion which caused it to be listed; and (iii) all other information or that portion which caused it to be listed, is included herewith.

b. Any patents, publications or other information which are listed on PTO/SB/08 which are not enclosed herewith were previously cited by or submitted to the PTO in one of the following applications which has been relied upon for an earlier filing date under 35 U.S.C. §120:

U.S. Serial Number

U.S. Filing Date

II.		<u>E EXPLANATION OF THE RELEVANCE</u> (check at least one box)
	a. 🛛	Except as may be indicated below in (b) of this section, all of the patents, publications or other information
		are in the English language (concise explanation not required).
	b. 🔲	A concise explanation of the relevance of all patents, publications or other information listed that is not in the
		English language is as follows:
	c. 🗌	The following additional information is provided for the Examiner's consideration:

III. CROSS REFERENCE TO RELATED APPLICATION(S)

The Examiner is advised that the following co-pending application(s) contain(s) subject matter that may be related to the present application. By bringing this (these) applications to the Examiner's attention, Applicant(s) does(do) not waive the confidentiality provisions of 35 U.S.C. §122.

Serial No. Filing Date Art Unit

FEES

IV. 🔲	THIS IDS a. b. c. d.	within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) (37 C.F.R. §1.97(b)(1)). No fee or statement is required. within three months of the date of entry of the national stage as set forth in § 1.491 in an international application (37 C.F.R. §1.97(b)(2)). No fee or statement is required. before the mailing date of a first Office Action on the merits (37 C.F.R. §1.97(b)(3)). No fee or statement is required. before the mailing date of a first Office Action after the filing of a request for continued examination under § 1.114 (37 C.F.R. § 1.97(b)(4)). No fee or statement is required.
V. 🔲	before th	EIS BEING FILED UNDER 37 C.F.R. §1.97(c): (check one box) e mailing date of any of a Final Office Action under 37 C.F.R. §1.113, a Notice of Allowance under 37 C.F.R. or an action that otherwise closes prosecution in the application (See 37 C.F.R. §1.97(c)). No statement; therefore, charge Deposit Account 503079, Freescale Semiconductor, Inc. the fee set forth in 37 C.F.R. §1.17(p). See the statement below. No fee is required.
VI. 🛚	THIS IDS on or bef 1) 2)	is IS BEING FILED UNDER 37 C.F.R. §1.97(d): ore payment of the issue fee and is accompanied by the following: a statement under 37 C.F.R. §1.97(e) as provided below; and charge Deposit Account 503079, Freescale Semiconductor, Inc. the petition fee set forth in §1.17(p).
VII.	STATEM The under a	ensigned hereby states that each item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of IDS; or no item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application, and to knowledge of the person signing the statement after making reasonable inquiry, no item of information contained in the IDS was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of this statement, or some of the items of information contained in the IDS were cited in a communication from a foreign Patent Office. As to this information, the undersigned states that each item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this IDS. As to the remaining information, the undersigned hereby states that no item of this remaining information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application or, to the knowledge of the person signing the statement after making reasonable inquiry, no item of information contained in the IDS was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of this statement.
VIII.	PAYMEN	A check in the amount of is enclosed for the above-identified fee(s). Please charge Deposit Account No. 503079, Freescale Semiconductor, Inc. in the amount of \$180.00 for the above-indicated fee(s). If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc. Two Copies of this paper are attached for Deposit Account charges and debits.

The above references are being cited only in the interests of candor and without any admission that they constitute statutory prior art or contain matter which anticipates the invention or which would render the same obvious, either singly or in a combination, to a person of ordinary skill in the art.

If the Examiner has any questions concerning this IDS, he/she is requested to contact the undersigned. If it is determined that this IDS has been filed under the wrong rule, the PTO is requested to consider this IDS under the proper rule (with a petition if necessary) and charge the appropriate fee to Deposit Account No. 503079, Freescale Semiconductor, Inc.

Respectfully submitted, Leo Mathew et al.

FREESCALE SEMICONDUCTOR, INC.

Customer Number 23125

Enclosures:

PTO/SB/08

References DA-DM

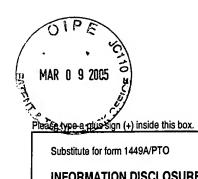
Other:

Robert L. King

Attorneyfor Applicant(s)

Reg. No. 30,185

Tel. (512) 996-6839



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Substitute fo	Substitute for form 1449A/PTO			Complete if Known		
			Application Number	10/074,732		
INFORMA	INFORMATION DISCLOSURE			Filing Date	February 13, 2002	
STATEME	STATEMENT BY APPLICANT		First Named Inventor	Leo Mathew et al.		
		7		Group Art Unit	2815	
(use as man)	sheets as necessary)			Examiner Name	Paul E. Brock II	
Sheet	1	of	1	Attorney Docket Number	SC11805TP	

Examiner	Cite No.	U.S. Patent Document		Name of Patentee or Applicant	Date of Publication of	Pages, Columns, Lines, Where Relevan
Initials*	1	Number Kind (if k		of Cited Document	Cited Document MM-DD-YYYY	Passages or Relevant Figures Appear
	DA	6,689,650	B2	Gambino et al.	02-10-2004	
	DB	6,583,469	B1	Fried et al.	06-24-2003	
•	DC	2003/0113970	A1	Fried et al.	06-19-2003	
	DD	6,566,682	B2	Forbes	05-20-2003	
	DE	6,531,350	B2	Satoh et al.	03-11-2003	
	DF	6,472,258	B1	Adkisson et al.	10-29-2002	
	DG	6,458,662	B1	Yu	10-01-2002	
	DH	6,433,609	B1	Voldman	08-13-2002	
	DI	6,424,001	B1	Forbes et al.	07-23-2002	
	DJ	6,414,356	B1	Forbes et al.	07-02-2002	
	DK	6,413,802	B1	Hu et al.	07-02-2002	
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	DM	6,355,961	B1	Forbes	03-12-2002	
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Examiner	Cite No.	No. Foreign Patent Document			Name of Patentee or	Date of Publication	Pages, Columns, Lines,	T 6
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Examiner	Date	
Signature	Considered	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation, if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See Kinds of U.S. Patent Documents, ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English Language Translation is attached.